

AMENDMENTS TO THE SPECIFICATION

Please amend the Specification as follows:

On page 8, please amend the paragraph beginning on line 7 as follows:

Fig. 15(a) is a partial layout diagram of LDMOSes wherein no trenches are made, the LDMOSes being arranged into multiple rows, and ~~Fig. 14(b) is~~ Fig. 15(b) is a partial layout diagram of LDMOSes wherein trenches are made, the LDMOSes being arranged into multiple rows.